Git hub Link: <https://github.com/paviabera/DWMsimulator>

-----> The Instructions to provide in the simulator:

* Overwrite AP0
* Overwrite AP1
* AP0 AP1
* AP0 LE
* AP0 RE
* AP1 LE
* AP1 LE
* shift\_left
* shift\_right
* Logic Operations:
  + AND
  + OR
  + NOR
  + NAND
  + XOR
  + XNOR
  + NOT -- ?
* TW:
  + Value:
  + SRC: AP0, AP1
  + Sink: AP1, LE, RE, AP0

Example: Value AP0 AP1 – write at (left) TRd start and shift data right

Value AP0 LE - write at (left) TRd start and shift data towards the left padding.

Value AP0 RE - write at (left) TRd start and shift data towards the right padding.

Value AP1 AP0 – write at (right) TRd end and shift data left

Value AP1 RE - write at (right) TRd end and shift data towards right padding.

Value AP1 LE - write at (right) TRd end and shift data towards left padding

Value AP0 – overwrite at left side (TRd start position)

Value AP1 -overwrite at right side (TRd end position)

Where value = result from the operations such as and/nor/nand etc.

* Read 0
* Read 1

Configuration file: Consists of the initial configuration.

TRd = 4

TRd\_start\_loc = 4

TRd\_end\_loc =

bit\_length

L = 10

memory

Notes:

Question:

1. Should the TW (value, source, sink) options be present for write instructions?

Yes

1. How will the not operation work?
2. Depending on NOT how will NAND and NOR operate?